

A parallel pipeline computer architecture for speech processing (Computer science)

Georgiou, Vassilios John

A Parallel Pipeline Computer Architecture For Speech Processing Computer Science

Guillaume Favre



A Parallel Pipeline Computer Architecture For Speech Processing Computer Science:

A Parallel Pipeline Computer Architecture for Speech Processing Vassilios John Georgiou,1984 Scientific and Technical Aerospace Reports ,1995 **The Publishers' Trade List Annual** ,1985 *Domain-Specific Computer Architectures for Emerging Applications* Chao Wang,2024-06-04 With the end of Moore's Law domain specific architecture DSA has become a crucial mode of implementing future computing architectures This book discusses the system level design methodology of DSAs and their applications providing a unified design process that guarantees functionality performance energy efficiency and real time responsiveness for the target application DSAs often start from domain specific algorithms or applications analyzing the characteristics of algorithmic applications such as computation memory access and communication and proposing the heterogeneous accelerator architecture suitable for that particular application This book places particular focus on accelerator hardware platforms and distributed systems for various novel applications such as machine learning data mining neural networks and graph algorithms and also covers RISC V open source instruction sets It briefly describes the system design methodology based on DSAs and presents the latest research results in academia around domain specific acceleration architectures Providing cutting edge discussion of big data and artificial intelligence scenarios in contemporary industry and typical DSA applications this book appeals to industry professionals as well as academicians researching the future of computing in these areas Computer Books and Serials in Print ,1985 Handbook of Signal Processing Systems Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2018-10-13 In this new edition of the Handbook of Signal Processing Systems many of the chapters from the previous editions have been updated and several new chapters have been added The new contributions include chapters on signal processing methods for light field displays throughput analysis of dataflow graphs modeling for reconfigurable signal processing systems fast Fourier transform architectures deep neural networks programmable architectures for histogram of oriented gradients processing high dynamic range video coding system on chip architectures for data analytics analysis of finite word length effects in fixed point systems and models of architecture There are more than 700 tables and illustrations in this edition over 300 are in color This new edition of the handbook is organized in three parts Part I motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems Part II discusses architectures for implementing these applications and Part III focuses on compilers as well as models of computation and their associated design tools and methodologies *Languages and Compilers for Parallel Computing* James Brodman,Peng Tu,2015-04-30 This book constitutes the thoroughly refereed post conference proceedings of the 27th International Workshop on Languages and Compilers for Parallel Computing LCPC 2014 held in Hillsboro OR USA in September 2014 The 25 revised full papers were carefully reviewed and selected from 39 submissions The papers are organized in topical sections on accelerator programming algorithms for parallelism compilers debugging vectorization **Energy Research Abstracts** ,1979

Application Specific Processors Earl E. Swartzlander Jr.,2012-12-06 Application Specific Processors is written for use by engineers who are developing specialized systems application specific systems Traditionally most high performance signal processors have been realized with application specific processors The explanation is that application specific processors can be tailored to exactly match the usually very demanding application requirements The result is that no processing power is wasted for unnecessary capabilities and maximum performance is achieved A disadvantage is that such processors have been expensive to design since each is a unique design that is customized to the specific application In the last decade computer aided design systems have been developed to facilitate the development of application specific integrated circuits The success of such ASIC CAD systems suggests that it should be possible to streamline the process of application specific processor design Application Specific Processors consists of eight chapters which provide a mixture of techniques and examples that relate to application specific processing The inclusion of techniques is expected to suggest additional research and to assist those who are faced with the requirement to implement efficient application specific processors The examples illustrate the application of the concepts and demonstrate the efficiency that can be achieved via application specific processors The chapters were written by members and former members of the application specific processing group at the University of Texas at Austin The first five chapters relate to specific arithmetic which often is the key to achieving high performance in application specific processors The next two chapters focus on signal processing systems and the final chapter examines the interconnection of possibly disparate elements to create systems

Masters Theses in the Pure and Applied Sciences Wade H. Shafer,2012-12-06 Masters Theses in the Pure and Applied Sciences was first conceived published and disseminated by the Center for Information and Numerical Data Analysis and Synthesis CINDAS at Purdue University in 1957 starting its coverage of theses with the academic year 1955 Beginning with Volume 13 the printing and dissemination phases of the activity were transferred to University Microfilms Xerox of Ann Arbor Michigan with the thought that such an arrangement would be more beneficial to the academic and general scientific and technical community After five years of this joint undertaking we had concluded that it was in the interest of all concerned if the printing and distribution of the volumes were handled by an international publishing house to assure improved service and broader dissemination Hence starting with Volume 18 Masters Theses in the Pure and Applied Sciences has been disseminated on a worldwide basis by Plenum Publishing Corporation of New York and in the same year the coverage was broadened to include Canadian universities All back issues can also be ordered from Plenum We have reported in Volume 32 thesis year 1987 a total of 12 483 theses titles from 22 Canadian and 176 United States universities We are sure that this broader base for these titles reported will greatly enhance the value of this important annual reference work While Volume 32 reports theses submitted in 1987 on occasion certain universities do report theses submitted in previous years but not reported at the time

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