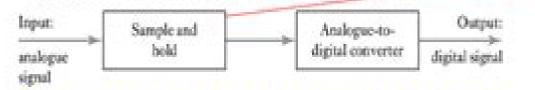
Analog to Digital Convertion

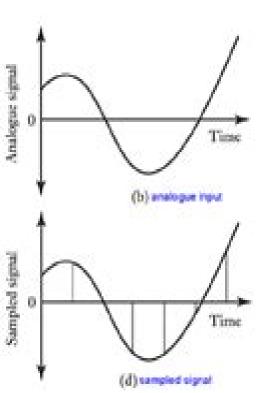
Involves converting the analogue signals into binary words.

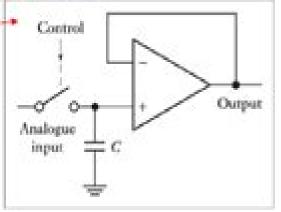
The basic elements of ADC is shown below:

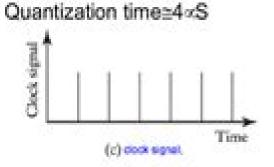


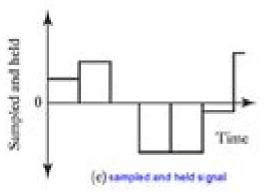
A clock supplies regular signal pulses to the ADC and every time it receives a pulse it samples the analogue signal

A sample and hold unit is used to hold each sampled value until the next pulse occurs. The S/H unit is necessary because the AD converter requires a finite amount of time termed the conversion time, to convert the analogue signal into a digital one



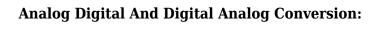






Analog Digital And Digital Analog Conversion

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