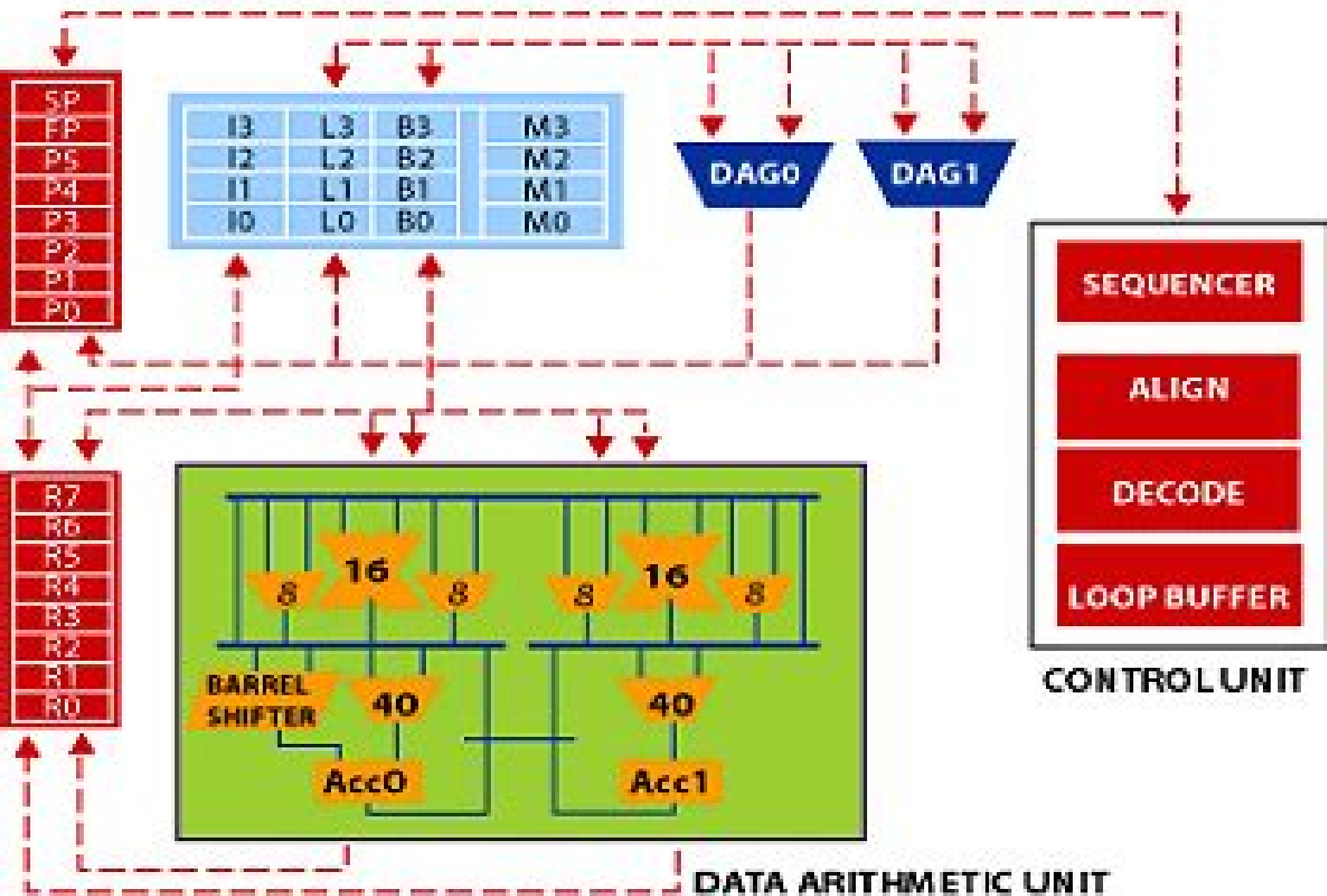


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BLACKfin™ Processor Architecture Core

1632bit Embedded Processors

Prabhat Mishra, Nikil Dutt



1632bit Embedded Processors:

Customizable Embedded Processors Paolo Ienne, Rainer Leupers, 2006-08-30 Customizable processors have been described as the next natural step in the evolution of the microprocessor business a step in the life of a new technology where top performance alone is no longer sufficient to guarantee market success Other factors become fundamental such as time to market convenience energy efficiency and ease of customization This book is the first to explore comprehensively one of the most fundamental trends which emerged in the last decade to treat processors not as rigid fixed entities which designers include as is in their products but rather to build sound methodologies to tailor fit processors to the specific needs of such products This book addresses the goal of maintaining a very large family of processors with a wide range of features at a cost comparable to that of maintaining a single processor First book to present comprehensively the major ASIP design methodologies and tools without any particular bias Written by most of the pioneers and top international experts of this young domain Unique mix of management perspective technical detail research outlook and practical implementation

Processor Description Languages Prabhat Mishra, Nikil Dutt, 2011-07-28 Efficient design of embedded processors plays a critical role in embedded systems design Processor description languages and their associated specification exploration and rapid prototyping methodologies are used to find the best possible design for a given set of applications under various design constraints such as area power and performance This book is the first comprehensive survey of modern architecture description languages and will be an invaluable reference for embedded system architects designers developers and validation engineers Readers will see that the use of particular architecture description languages will lead to productivity gains in designing particular application specific types of embedded processors Comprehensive coverage of all modern architecture description languages use the right ADL to design your processor to fit your application Most up to date information available about each architecture description language from the developers save time chasing down reliable documentation Describes how each architecture description language enables key design automation tasks such as simulation synthesis and testing fit the ADL to your design cycle

Advanced Memory Optimization Techniques for Low-Power Embedded Processors Manish Verma, Peter Marwedel, 2007-06-20 In a relatively short span of time computers have evolved from huge mainframes to small and elegant desktop computers and now to low power ultra portable handheld devices With each passing generation computers consisting of processors memories and peripherals become smaller and faster For example the first commercial computer UNIVAC costed 1 million dollars occupied 943 cubic feet space and could perform 1 905 operations per second 94 Now a processor present in an electric shaver easily outperforms the early mainframe computers The miniaturization is largely due to the efforts of engineers and scientists that made the expeditious progress in the microelectronic technologies possible According to Moore's Law 90 the advances in technology allow us to double the number of transistors on a single silicon chip every 18 months This has led to an exponential increase in the number of

transistors on a chip from 2 300 in an Intel 4004 to 42 millions in Intel Itanium processor 55 Moore's Law has withstood for 40 years and is predicted to remain valid for at least another decade 91

Not only the miniaturization and dramatic performance improvement but also the significant drop in the price of processors has led to a situation where they are being integrated into products such as cars, televisions and phones which are not usually associated with computers. This new trend has also been called the disappearing computer where the computer does not actually disappear but it is everywhere 85 Digital devices containing processors now constitute a major part of our daily lives. A small list of such devices includes microwave ovens, television sets, mobile phones, digital cameras, MP3 players and cars.

Whenever a system comprises of information processing digital devices to control or to augment its functionality such as a system is termed an embedded system. Therefore all the above listed devices can be also classified as embedded systems.

16-/32-bit Embedded Processor Handbook Intel Corporation, 1990 *Ultra-Low Energy Domain-Specific Instruction-Set Processors* Francky Catthoor, Praveen Raghavan, Andy Lambrechts, Murali Jayapala, Angeliki Kritikakou, Javed Absar, 2010-08-05 Modern consumers carry many electronic devices like a mobile phone, digital camera, GPS, PDA and an MP3 player. The functionality of each of these devices has gone through an important evolution over recent years with a steep increase in both the number of features as in the quality of the services that they provide. However, providing the required compute power to support an uncompromised combination of all this functionality is highly non-trivial. Designing processors that meet the demanding requirements of future mobile devices requires the optimization of the embedded system in general and of the embedded processors in particular as they should strike the correct balance between flexibility, energy efficiency and performance. In general, a designer will try to minimize the energy consumption as far as needed for a given performance with a sufficient flexibility. However, achieving this goal is already complex when looking at the processor in isolation but in reality the processor is a single component in a more complex system. In order to design such complex system successfully, critical decisions during the design of each individual component should take into account effect on the other parts with a clear goal to move to a global Pareto optimum in the complete multi-dimensional exploration space. In the complex global design of battery-operated embedded systems, the focus of Ultra-Low Energy Domain-Specific Instruction-Set Processors is on the energy-aware architecture, exploration of domain-specific instruction set processors and the co-optimization of the datapath architecture, foreground memory and instruction memory organisation with a link to the required mapping techniques or compiler steps at the early stages of the design. By performing an extensive energy breakdown experiment for a complete embedded platform, both energy and performance bottlenecks have been identified together with the important relations between the different components. Based on this knowledge, architecture extensions are proposed for all the bottlenecks. *16- and 32-bit Embedded Processors* Information Network, 1991 *I750, I860, I960 Processors and Related Products* Intel Corporation, 1994 *Eight-bit 80C51 Embedded Processors* Advance Micro Devices (Firm), 1989

Embedded Microcontrollers & Processors Intel Corporation, 1992 Low-Power Processors and Systems on Chips

Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

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