

GEOMETRIC MODELLING

Theoretical and Computational Basis towards Advanced CAD Applications

Edited by
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Nanoscale Silicon Devices Shunri Oda, David K. Ferry, 2018-09-03 Is Bigger Always Better Explore the Behavior of Very Small Devices as Described by Quantum Mechanics Smaller is better when it comes to the semiconductor transistor Nanoscale Silicon Devices examines the growth of semiconductor device miniaturization and related advances in material device circuit and system design and highlights the use of device scaling within the semiconductor industry Device scaling the practice of continuously scaling down the size of metal oxide semiconductor field effect transistors MOSFETs has significantly improved the performance of small computers mobile phones and similar devices The practice has resulted in smaller delay time and higher device density in a chip without an increase in power consumption This book covers recent advancements and considers the future prospects of nanoscale silicon Si devices It provides an introduction to new concepts including variability in scaled MOSFETs thermal effects spintronics based nonvolatile computing systems spin based qubits magnetoelectric devices NEMS devices tunnel FETs dopant engineering and single electron transfer new materials such as high k dielectrics and germanium and new device structures in three dimensions It covers the fundamentals of such devices describes the physics and modeling of these devices and advocates further device scaling and minimization of energy consumption in future large scale integrated circuits VLSI Additional coverage includes Physics of nm scaled devices in terms of quantum mechanics Advanced 3D transistors tri gate structure and thermal effects Variability in scaled MOSFET Spintronics on Si platform NEMS devices for switching memory and sensor applications The concept of ballistic transport The present status of the transistor variability and more An indispensable resource Nanoscale Silicon Devices serves device engineers and academic researchers including graduate students in the fields of electron devices solid state physics and nanotechnology *Proceedings of the Sixth International Conference on Computer Supported Cooperative Work in Design* Shen Weiming, Institute of Electrical and Electronics Engineers. Canadian Region, National Research Council Canada, University of Waterloo, University of Western Ontario, 2001 Computer supported co operative work CSCW is a research area that aims at integrating the works of several people involved in a common goal inside a co operative universe through the sharing of resources in an efficient way This report contains the papers presented at a conference on CSCW in design Topics covered include techniques methods and tools for CSCW in design social organization of the CSCW process integration of methods co operation in virtual enterprises and electronic businesses CSCW in design interaction between the CSCW approach and knowledge reuse as found in knowledge management intelligent agent Internet World Wide Web and CSCW in design and applications test beds **High-dimensional Nonlinear Diffusion Stochastic Processes** Yevgeny Mamontov, M. Willander, 2001 Annotation This book is one of the first few devoted to high dimensional diffusion stochastic processes with nonlinear coefficients These processes are closely associated with large systems of Ito's stochastic differential

equations and with discretized in the parameter versions of Ito's stochastic differential equations that are nonlocally dependent on the parameter. The latter models include Ito's stochastic integro-differential partial differential and partial integro-differential equations. The book presents the new analytical treatment which can serve as the basis of a combined analytical-numerical approach to greater computational efficiency. Some examples of the modelling of noise in semiconductor devices are provided.

Index of Conference Proceedings British Library. Document Supply Centre, 2003. Extended Abstracts of 1998 Sixth International Workshop on Computational Electronics, 1998. *Silicon Nanoelectronics* Shunri Oda, David Ferry, 2017-12-19. Technological advancement in chip development primarily based on the downscaling of the feature size of transistors is threatening to come to a standstill as we approach the limits of conventional scaling. For example, when the number of electrons in a device's active region is reduced to less than ten electrons, quantum fluctuation errors will occur, and when gate insulator thickness becomes too insignificant to block quantum mechanical tunneling, unacceptable leakage will occur. Fortunately, there is truth in the old adage that whenever a door closes, a window opens somewhere else. In this case, that window opening is nanotechnology. *Silicon Nanoelectronics* takes a look at the recent development of novel devices and materials that hold great promise for the creation of still smaller and more powerful chips. Silicon nanodevices are positioned to be particularly relevant in consideration of the existing silicon process infrastructure already in place throughout the semiconductor industry and silicon's consequent compatibility with current CMOS circuits. This is reinforced by the nearly perfect interface that can exist between natural oxide and silicon. Presenting the contributions of more than 20 leading academic and corporate researchers from the United States and Japan, *Silicon Nanoelectronics* offers a comprehensive look at this emergent technology. The text includes extensive background information on the physics of silicon nanodevices and practical CMOS scaling. It considers such issues as quantum effects and ballistic transport and resonant tunneling in silicon nanotechnology. A significant amount of attention is given to the all-important silicon single-electron transistors and the devices that utilize them. In offering an update of the current state of the art in the field of silicon nanoelectronics, this volume serves well as a concise reference for students, scientists, engineers, and specialists in various fields.

2002 International Conference on Computational Nanoscience and Nanotechnology Matthew Laudon, 2002. The world's most comprehensive and up-to-date collection of Nanotechnology and Nanoscience technical papers. Technical Proceedings of the Nanotech 2002 and the International Conference on Computational Nanoscience and Nanotechnology. Nanotech Vol 1. Sequence and Biological Structure. Computer Aided Drug Design. Biological Conduction Processes. Biotechnology. Micro and Nano Fluidic Systems. Soft Condensed Matter. Extended Scale Atomistics. Quantum Effects. Quantum Devices. Spintronics. Mechanical Properties at the Nanoscale. Molecular and Nano Electronics. Condensed Matter Phenomena. Process Modeling. Nanotechnology Materials and Nanostructures. Studies Nano Particles and Molecules. Papers taken from the 2002 Nanotechnology Conference and Trade Show, San Juan, Puerto Rico, April 2002.

Extended Abstracts of 1998 Sixth International Workshop on Computational Electronics IEEE Electron Devices

Society, 1998 IWCE covers all aspects of advanced simulation of semiconductor process devices and electronic transport in semiconductors particularly those aspects that utilize intensive computational resources Topics include advances in 2 D and 3 D standard simulation and particle simulation methods **Advances in Electrical Engineering and Computational Science** Len Gelman, 2009-04-21 Advances in Electrical Engineering and Computational Science contains sixty one revised and extended research articles written by prominent researchers participating in the conference Topics covered include Control Engineering Network Management Wireless Networks Biotechnology Signal Processing Computational Intelligence Computational Statistics Internet Computing High Performance Computing and industrial applications Advances in Electrical Engineering and Computational Science will offer the state of art of tremendous advances in electrical engineering and computational science and also serve as an excellent reference work for researchers and graduate students working with on electrical engineering and computational science

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