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Asynchronous Digital Circuit Design Graham Birtwistle, Alan Davis, 2013-04-17 As the costs of power and timing become increasingly difficult to manage in traditional synchronous systems designers are being forced to look at asynchronous alternatives Based on reworked and expanded papers from the VII Banff Higher Order Workshop this volume examines asynchronous methods which have been used in large circuit design ranging from initial formal specification to more standard finite state machine based control models Written by leading practitioners in the area the papers cover many aspects of current practice including practical design silicon compilation and applications of formal specification It also includes a state of the art survey of asynchronous hardware design The resulting volume will be invaluable to anyone interested in designing correct asynchronous circuits which exhibit high performance or low power operation

Asynchronous Circuit Design Chris J. Myers, 2001-07-23 With asynchronous circuit design becoming a powerful tool in the development of new digital systems circuit designers are expected to have asynchronous design skills and be able to leverage them to reduce power consumption and increase system speed This book walks readers through all of the different methodologies of asynchronous circuit design emphasizing practical techniques and real world applications instead of theoretical simulation The only guide of its kind it also features an ftp site complete with support materials Market Electrical Engineers Computer Scientists Device Designers and Developers in industry

Principles of Asynchronous Circuit Design Jens Sparsø, Stephen Bo Furber, 2001-12-31 Principles of Asynchronous Circuit Design A Systems Perspective addresses the need for an introductory text on asynchronous circuit design Part I is an 8 chapter tutorial which addresses the most important issues for the beginner including how to think about asynchronous systems Part II is a 4 chapter introduction to Balsa a freely available synthesis system for asynchronous circuits which will enable the reader to get hands on experience of designing high level asynchronous systems Part III offers a number of examples of state of the art asynchronous systems to illustrate what can be built using asynchronous techniques The examples range from a complete commercial smart card chip to complex microprocessors The objective in writing this book has been to enable industrial designers with a background in conventional clocked design to be able to understand asynchronous design sufficiently to assess what it has to offer and whether it might be advantageous in their next design task

Introduction to Asynchronous Circuit Design Jens Sparsø, 2020-06-18 This book is an introduction to the design of asynchronous circuits It is an updated and significantly extended version of an eight chapter tutorial that first appeared as Part I in the book Principles of asynchronous circuit design A systems perspective edited by Sparsø and Furber 2001 a book that has become a standard reference on the topic The extensions include improved coverage of data flow components a new chapter on two phase bundled data circuits a new chapter on metastability arbitration and synchronization and a new chapter on performance analysis using timed Petri nets With these extensions the text now provides a more complete coverage of the topic and it is now made available as a stand

alone book The book is a beginner's text and the amount of formal notation is deliberately kept at a minimum using instead plain English and graphical illustrations to explain the underlying intuition and reasoning behind the concepts and methods covered The book targets senior undergraduate and graduate students in Electrical and Computer Engineering and industrial designers with a background in conventional clocked digital design who wish to gain an understanding of asynchronous circuit design

Principles of Asynchronous Circuit Design Jens Sparsø, Steve Furber, 2013-04-17 Principles of Asynchronous Circuit Design A Systems Perspective addresses the need for an introductory text on asynchronous circuit design Part I is an 8 chapter tutorial which addresses the most important issues for the beginner including how to think about asynchronous systems Part II is a 4 chapter introduction to Balsa a freely available synthesis system for asynchronous circuits which will enable the reader to get hands on experience of designing high level asynchronous systems Part III offers a number of examples of state of the art asynchronous systems to illustrate what can be built using asynchronous techniques The examples range from a complete commercial smart card chip to complex microprocessors The objective in writing this book has been to enable industrial designers with a background in conventional clocked design to be able to understand asynchronous design sufficiently to assess what it has to offer and whether it might be advantageous in their next design task

Completion Detection in Asynchronous Circuits Pallavi Srivastava, 2022-11-08 This book is intended for designers with experience in traditional clocked circuit design seeking information about asynchronous circuit design in order to determine if it would be advantageous to adopt asynchronous methodologies in their next design project The author introduces a generic approach for implementing a deterministic completion detection scheme for asynchronous bundled data circuits that incorporates a data dependent computational process taking advantage of the average case delay The author validates the architecture using a barrel shifter as shifting is the basic operation required by all the processors The generic architecture proposed in this book for a deterministic completion detection scheme for bundled data circuits will facilitate researchers in considering the asynchronous design style for developing digital circuits

Asynchronous Circuit Applications Jia Di, Scott C. Smith, 2019-11-07 Unlike conventional synchronous circuits asynchronous circuits are not coordinated by a clocking signal but instead use handshaking protocols to control circuit behaviour Asynchronous circuits have been found to offer several advantages including high energy efficiency flexible timing requirements high modularity low noise EMI and robustness to PVT variations At the same time growing pressures on the electronics industry for ever smaller more efficient ICs are pushing the limits of conventional circuit technologies These factors are spurring growing interest in asynchronous circuits amongst both the academic research and commercial R D communities

Asynchronous Circuit Design for VLSI Signal Processing Teresa H. Meng, Sharad Malik, 2011-06-27 Asynchronous Circuit Design for VLSI Signal Processing is a collection of research papers on recent advances in the area of specification design and analysis of asynchronous circuits and systems This interest in designing digital computing systems without a global clock is prompted

by the ever growing difficulty in adopting global synchronization as the only efficient means to system timing Asynchronous circuits and systems have long held interest for circuit designers and researchers alike because of the inherent challenge involved in designing these circuits as well as developing design techniques for them The frontier research in this area can be traced back to Huffman s publications *The Synthesis of Sequential Switching Circuits* in 1954 followed by Unger s book *Asynchronous Sequential Switching Circuits* in 1969 where a theoretical foundation for handling logic hazards was established In the last few years a growing number of researchers have joined force in unveiling the mystery of designing correct asynchronous circuits and better yet have produced several alternatives in automatic synthesis and verification of such circuits This collection of research papers represents a balanced view of current research efforts in the design synthesis and verification of asynchronous systems

Digital Logic Design Brian Holdsworth, Clive Woods, 2002-11-01 New updated and expanded topics in the fourth edition include EBCDIC Grey code practical applications of flip flops linear and shaft encoders memory elements and FPGAs The section on fault finding has been expanded A new chapter is dedicated to the interface between digital components and analog voltages A highly accessible comprehensive and fully up to date digital systems text A well known and respected text now revamped for current courses Part of the Newnes suite of texts for HND 1st year modules

Designing Asynchronous Circuits Using NULL Convention Logic (NCL) Scott C. Smith, Jia Di, 2009 Designing Asynchronous Circuits using NULL Convention Logic NCL begins with an introduction to asynchronous clockless logic in general and then focuses on delay insensitive asynchronous logic design using the NCL paradigm The book details design of input complete and observable dual rail and quad rail combinational circuits and then discusses implementation of sequential circuits which require datapath feedback Next throughput optimization techniques are presented including pipelining embedding registration early completion and NULL cycle reduction Subsequently low power design techniques such as wavefront steering and Multi Threshold CMOS MTCMOS for NCL are discussed The book culminates with a comprehensive design example of an optimized Greatest Common Divisor circuit Readers should have prior knowledge of basic logic design concepts such as Boolean algebra and Karnaugh maps After studying this book readers should have a good understanding of the differences between asynchronous and synchronous circuits and should be able to design arbitrary NCL circuits optimized for area throughput and power

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