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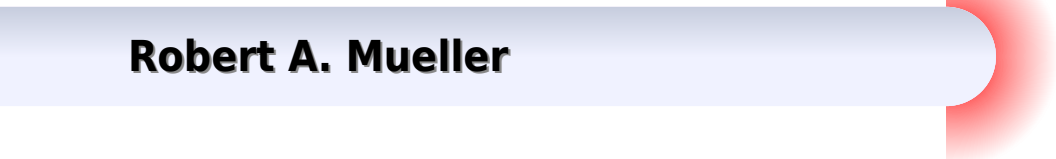
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chips The gap between system design and silicon design can only be bridged by new design CAD The idea of a silicon compiler translating a behavioural system specification directly into silicon was born from the awareness that the ability to fabricate chips is indeed outrunning the ability to design them At this moment CAD is one order of magnitude behind schedule Conceptual CAD is the keyword to mastering the design complexity in ASIC design and the topic of this book

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