Special Section on the International Conference on Microelectronic Test Structures

HE continued increase in process complexity, combined with reduction in process latitude, creates ever-growing challenges to the success of the semiconductor industry. As we cannot control what we do not measure, the understanding and use of test structures continues to be central to our industry's success.

Since 1988, the International Conference on Microelectronic Test Structures (ICMTS) has provided a forum for the test structure design and user community to meet and discuss these important challenges. To this end, ICMTS focuses on the design, fabrication, and characterization of test structures for process and material evaluation, reliability and process failure analysis, manufacturing control, device and circuit modeling, as well as associated measurement techniques and data analysis. ICMTS is one of the few international IEEE-sponsored conferences that moves between Europe, Asia, and the United States in a threeyear cycle. In 2006, the 19th ICMTS was held in Austin, TX, USA. Over 40 regular papers were presented in nine sessions, in addition to three invited papers. This special section presents a selection of ten papers from the meeting. Papers were selected based upon the merit of the work and their ability to demonstrate the broad spectrum of test structure design and use by a worldwide community. Authors were asked to extend the scope

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of their work presented at ICMTS by adding the latest developments on the progress of their chosen field of study to their paper presented in this special section.

Included papers feature work geared to furthering the understanding of systematic and parametric yield loss characteristics of advanced CMOS nodes, with novel array and/or ac test structures. In addition, two papers are showcased discussing extensions of standard CMOS to MEMS. Other papers highlight the ongoing efforts required to advance the understanding of fundamental process and device issues such as lithography (in this case alternating phase shift masks), device interface understanding, and device heating.

I would like to thank the authors for their attentiveness to detail and schedule and also the reviewers who take time out of their busy days to referee and provide the excellent feedback to the authors that helps us maintain the high quality levels we have come to know in IEEE publications. Finally, I would like to thank Prof. Anthony Walton for the great amount of help in the ongoing stewardship of the ICMTS, but specifically for his support in preparation of this special section.

> GREG YERGC, Guest Editor ARM Austin, TX 78746 USA



Greg Yeric (SM'00) received the Ph.D. degree in microelectronics from the University of Texas, Austin, in 1993.

Since then, has worked in the development of new semiconductor technology, silicon characterization, yield improvement, and design for manufacturability (DFM). In the past ten years, he has been a DFM Technologist and Marketing Analyst with Synopsys, Inc., Chief Technologist at HPL Technologies, and a Senior Technologist at TestChip Technologies. In these capacities he has contributed to novel silicon characterization, EDA, and yield analysis technologies. He and his organizations have conducted over 300 technology development projects, for the majority of the world's semiconductor manufacturers. He began his career in embedded NVM process integration and reliability qualification at Motorola's Advanced Products Research and Development Laboratories (APRDL).

Dr. Yeric was Technical Chair of the IEEE International Conference on Microelectronic Test Structures (ICMTS), in 2006, and will be General Chair in 2009. He has been an Invited Speaker

and author at the IEEE DAC, DFM&Y, and VLSI Test Conferences, as well as Semicon West and Solid State Technology Magazine.

1998 Ieee International Conference On Microelectronic Test Structures

G Thomas

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Embracing the Tune of Term: An Mental Symphony within **1998 Ieee International Conference On Microelectronic Test**Structures

In some sort of consumed by monitors and the ceaseless chatter of immediate connection, the melodic elegance and emotional symphony created by the prepared term usually disappear into the background, eclipsed by the constant sound and interruptions that permeate our lives. Nevertheless, set within the pages of **1998 Ieee International Conference On Microelectronic Test Structures** a wonderful literary value brimming with raw feelings, lies an immersive symphony waiting to be embraced. Crafted by an outstanding musician of language, this interesting masterpiece conducts viewers on a mental trip, well unraveling the hidden tunes and profound impact resonating within each carefully constructed phrase. Within the depths of this moving assessment, we shall examine the book is key harmonies, analyze their enthralling publishing model, and surrender ourselves to the profound resonance that echoes in the depths of readers souls.

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