

Control Unit

Instruction Stream

Processor 1

Processor 2

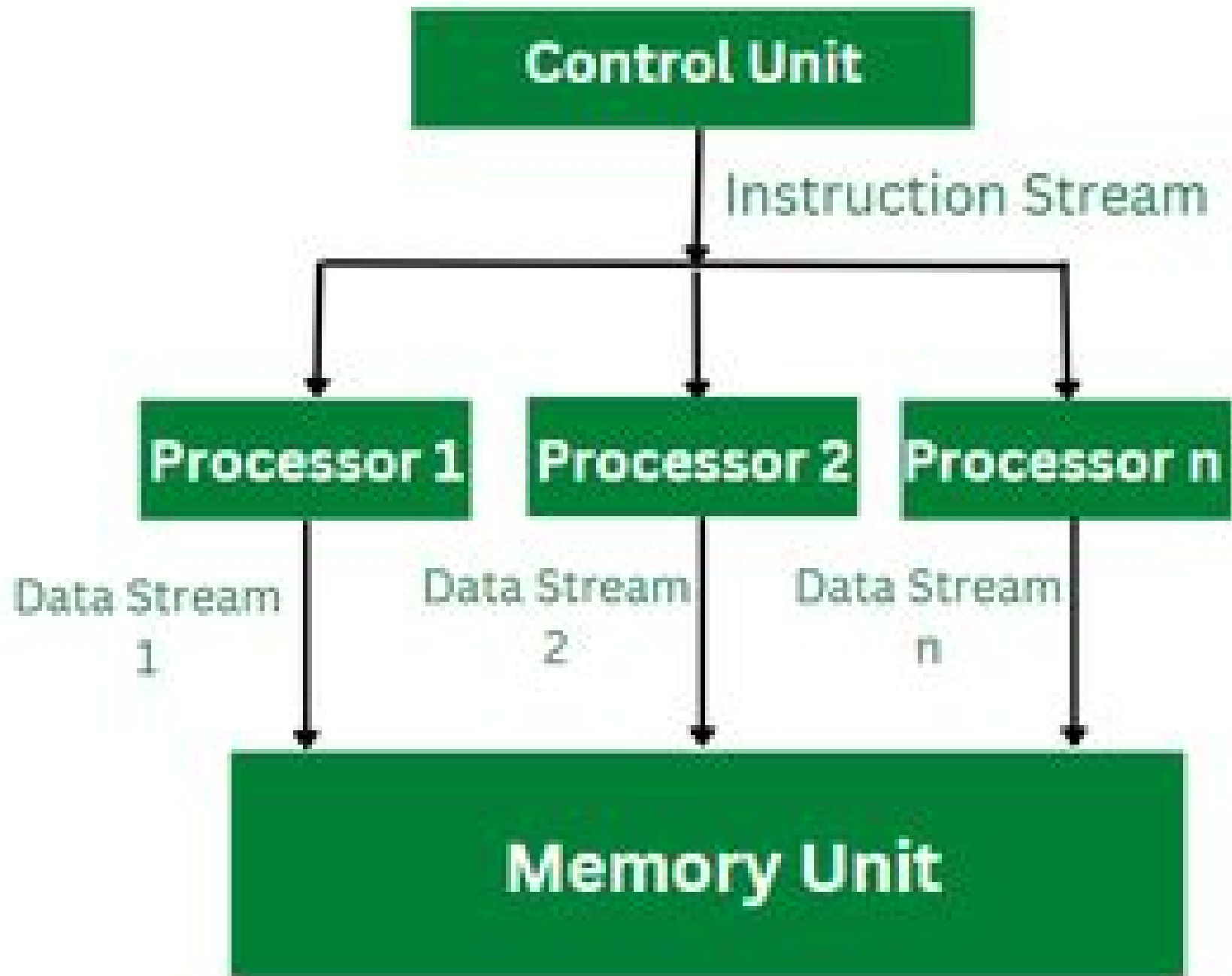
Processor n

Data Stream
1

Data Stream
2

Data Stream
n

Memory Unit



Abstract Machine Models For Highly Parallel Computers

C Cleary



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